IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re Patent Application of:)	
Shunpei YAMAZAKI)	Examiner: K. Moore
Serial No. 10/772,267)	Group Art Unit: 1792
Filed: February 6, 2004)	Confirmation No.: 4444
For: SEMICONDUCTOR MANUFACTURING DEVICE)	

INFORMATION DISCLOSURE STATEMENT

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initial a copy of this form be returned to the undersigned.

KR 2001-0093073 was cited in counterpart Korean Application No. 2005-7013795 mailed November 16, 2010.

Applicants wish to bring to the Examiner's attention the following family information. U.S. Patent No. 7,629,033, U.S. Publication No. 2002/0002948 and U.S. Publication No. 2007/0251453 are in the same family as KR 2001-00993073, but these family patent documents were not cited in the foreign Office Action noted above.

The undersigned certifies that each item of information contained in this information disclosure statement (with the exception of family patent documents noted above and listed on the attached PTO-1449) was first cited in a communication from a foreign patent office in connection with a counterpart foreign application not more than three (3) months prior to the filing of this statement.

The Commissioner is hereby authorized to charge the \$180 fee and any additional fees connected with this filing, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By: /Jeffrey L. Costellia, Reg.# 35483/ Jeffrey L. Costellia Registration No. 35,483

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